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Docket: 0756-1650

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on June 15, 1998.


Cameron S. Gilligan#12
7/8/99
Cayton

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of)
Shunpei YAMAZAKI et al.)
Serial No. 08/847,314) Art Unit: 2814
Filed: April 23, 1997) Examiner: G. Munson
For: LASER PROCESSING METHOD,)
METHOD FOR FORMING A FLASH) Customer Correction Branch
MEMORY, INSULATED GATE)
SEMICONDUCTOR DEVICE AND)
METHOD FOR FORMING THE SAME) Date: June 15, 1998

Attention: Application Processing DivisionRECEIVED
99 APR - 1 PM 12:59
TECHNOLOGY CENTER 2800**REQUEST FOR SECOND CORRECTED FILING RECEIPT**

Honorable Assistant Commissioner of Patents and Trademarks
Washington, D. C. 20231

Sir:

A Corrected Filing Receipt was received by the undersigned attorney of record in the above-identified application. On reviewing the Corrected Filing Receipt, several errors were noted in the "Continuing Data" as follows.

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Docket: 0756-1650

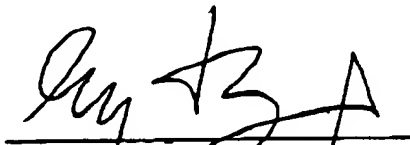
In the "Continuing Data", the subject application is a divisional application of Serial No. 08/692,227, filed August 7, 1996, which in turn is a divisional application of Serial No. 08/355,652, filed December 14, 1994, which in turn is a divisional application of Serial No. 08/142,048, filed October 28, 1993. The "Continuing Data" should read as follows:

This appln is a	DIV of	08/692,227	08/07/96	
<u>Which is a</u>	<u>DIV of</u>	<u>08/355,652</u>	<u>12/14/94</u>	<u>PAT 5,569,615</u>
<u>Which is a</u>	<u>DIV of</u>	<u>08/142,048</u>	<u>10/28/93</u>	<u>PAT 5,541,138</u>

Submitted herewith is a copy of the Filing Receipt with the above-noted corrections indicated thereon.

In view of the above, it is believed that a Second Corrected Filing Receipt is in order to correct the "Continuing Data" in the above-referenced application. Thus, it is requested that a Second Corrected Filing Receipt be issued.

Respectfully submitted,



Gerald J. Ferguson, Jr.
Registration No. 23,016

SIXBEY, FRIEDMAN, LEEDOM & FERGUSON, P.C.
2010 Corporate Ridge, Suite 600
McLean, Virginia 22102
(703) 790-9110

PTO-103X
(Rev. 8-25)FILING RECEIPT
CORRECTED

UNITED STATES DEPARTMENT OF COMMERCE
Patent and Trademark Office
ASSISTANT SECRETARY AND COMMISSIONER
OF PATENTS AND TRADEMARKS
Washington, D.C. 20231

APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTORNEY DOCKET NO.	DRWGS	TOT CL	IND CL
08/847,314	04/23/97	2814	\$1,098.00	0756-1650	8	24	6

SIXBEY FRIEDMAN LEEDOM & FERGUSON
2010 CORPORATE RIDGE
SUITE 600
MCLEAN VA 22102

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Application Processing Division's Customer Correction Branch within 10 days of receipt. Please provide a copy of the Filing Receipt with the changes noted thereon.

Applicant(s)

SHUNPEI YAMAZAKI, TOKYO, JAPAN; YASUHIKO TAKEMURA,
KANAGAWA, JAPAN.

CONTINUING DATA AS CLAIMED BY APPLICANT-

THIS APPLN IS A DIV OF 08/692,227 08/07/96

~~WHICH IS A DIV OF 08/335,652 11/08/94 PAT 5,319,799~~

WHICH IS A DIV OF 08/355,652 12/14/94 PAT 5,569,615

~~WHICH IS A DIV OF 08/142,048 10/28/93 PAT 5,541,138~~

FOREIGN APPLICATIONS- JAPAN

4-316138

10/30/92

TITLE

LASER PROCESSING METHOD, METHOD FOR FORMING A FLASH MEMORY, INSULATED
GATE SEMICONDUCTOR DEVICE AND METHOD FOR FORMING THE SAME

PRELIMINARY CLASS: 257

